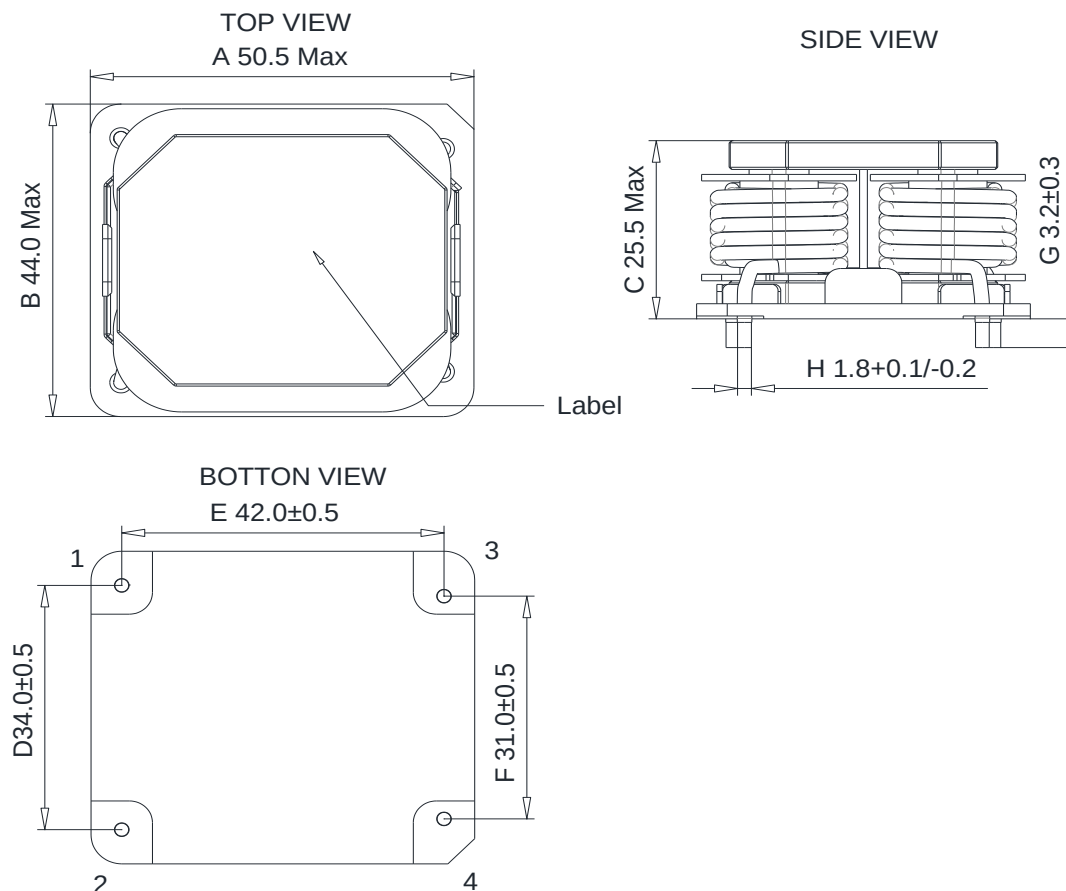


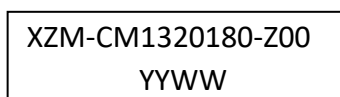
1. OUTLINE DRAWING(外形图): Unit(单位): mm (毫米)



NOTES(注):

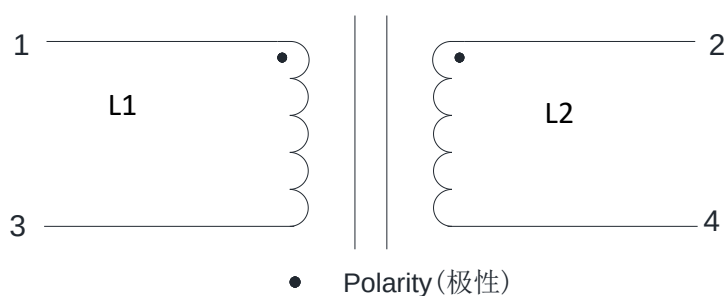
- ◆PCB drill hole is recommended to be 2.4 mm (建议 PCB 板开孔孔径 2.4mm)
- ◆If PIN layout and footprint have slightly deviation, please refers to actual PCB assembly, the ones can be normally inserted into PCB is qualified (PIN 距、排距尺寸测量有偏差时, 以 PCB 下板实装确认, 可正常下板为合格)
- ◆The Pin length doesn't include the solder tip (PIN 脚长度不包括锡尖),
- ◆Pin length is measured from the highest point of the bottom surface(PIN 长从底板面的最高点起量).
- ◆Fix the core,base and pins with glue (用胶水固定磁芯、底座和引脚).

2. MARKING (标记图):



YY:Year/年
WW:Week/周

3. SCHEMATIC(原理图):



4. ELECTRICAL SPECIFICATION(电气特性):

TEST TEMPERATURE(测试温度)@25±2℃:

Item 项目	Terminal 端子	Specification 规格值	Test condition 测试条件
Inductance 感量(uH)	1—3/2--4	1.0mH Min	100KHz,0.3V,SER
Leakage inductance 漏感(uH)	1—3 (short 2,4)	20 uH Min	100KHz,0.3V,SER
DCR 直流电阻(Ω)	1--3	8.5mΩ Max	Ta=25℃
	2--4	8.5mΩ Max	
Insulation Resistance 绝缘电阻	L1—L2	100 MΩ Min	1000VDC, 3sec
Dielectric strength 绝缘耐压	L1—L2	3410VDC 1min for qualification	3mA
		3410VDC 2sec in mass production	
	L1/L2—CORE	1500VDC 1min for qualification	
		1500VDC 2sec in mass production	

5. PRECAUTIONS FOR USE (产品使用注意事项):

Ambient temperature range(环境温度范围): -40℃ to +100℃;

Maximum temperature rise 65℃@18A at coil,the temperature rise and current can be higher with reduced ambient temperature. (线圈最高温升 65℃@18A, 环境温度越低可允许的温升和电流更高);

Storage temperature range(保存温度范围): -40℃ to +125℃;

Resistance to solder heat(焊锡耐热性): +260±5℃, 5±1Sec;

Rohs compliant directive 2011/65/EU(产品符合欧盟 ROHS 2011/65/EU 指令);

Rated current(额定电流): 18A DC;

Product compliance to AEC-Q200(产品符合 AEC-Q200 标准);

6. Electrical Parameters(Graph) 电气参数曲线图

This graph is only for R&D reference 此曲线图仅研发做参考用

